- MANUAL IDENTIFICATION -

Model Number:8447ADate Printed:April 1970Part Number:08447-90003

This supplement contains important information for correcting manual errors and for adapting the manual to instruments containing improvements made after the printing of the manual.

To use this supplement:

Make all ERRATA corrections

Make all appropriate serial number related changes indicated in the tables below.

1145A00	refix or Number 356A thru)1566, and	Make M 1	anual Changes	Serial Prefix or Number 1616A	Make Manual Changes 1, 2, 3
-01567 th	18 thru -01565, ru -01570, 01572 '5, and 1529A	1, 2			
prefix					

▶ NEW ITEM

ERRATA

Page 1-1, Table 1-1:

Add Reverse Isolation: >30 dB

Page 4-2, Paragraph 4-6:

Add 10 dB attenuator (2 required) HP 8491A Opt. 010 to the equipment list. Change specification for "Output Level (Compression)" to >+7 dBm at 1-dB compression point.

Page 4-3, Paragraph 4-6:

Change step 6 to read as follows:

To check flatness, set the signal level at Channel A to -30 dBm. Connect a 10 dB attenuator to each tee, between the tee and the adapter. Note the level difference between Channel A and Channel B as the generator frequency is tuned from 10 MHz to 400 MHz. Keep the input level to the amplifier (CHANNEL A) constant and note the variation in Channel B. Channel B should change >1 dB (±0.5 dB).

Page 4-5, Paragraph 4-7: Under SPECIFICATION: Change "parts" to "ports."

NOTE

Manual change supplements are revised as often as necessary to keep manuals as current and accurate as possible. Hewlett-Packard recommends that you periodically request the latest edition of this supplement. Free copies are available from all HP offices. When requesting copies quote the manual identification information from your supplement, or the model number and print date from the title page of the manual.



25 MAY 1976

4 Pages

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ERRATA (Cont'd)

Page 4-10, PERFORMANCE TESTS: Add the following performance test:

PERFORMANCE TESTS

4-10. REVERSE ISOLATION

SPECIFICATION:

Reverse Isolation: > 30 dB

DESCRIPTION:

Using a signal generator and a vector voltmeter, a known signal level loss is measured from OUTPUT to INPUT port.

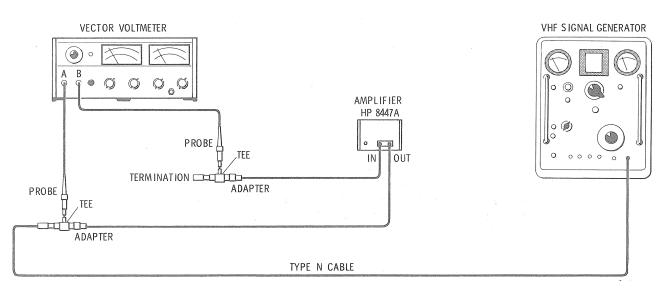


Figure 4-8. Reverse Isolation Test Setup

EQUIPMENT:

Signal Generator		۰.		•	•	•			•		•	٠	۰	۰		٠	•	۰		٠	.HP 608E/F
Vector Voltmeter .	•	•	•				•	•	•								•	•	۰	•	. HP 8405A
Feedthrough Tee (2).	•	•	٠	•	•	•		•	•	•	•	•		•	•	•	•	٠	•		HP 11536A
Termination																					
Adapter	•	•		•			· .·	ø	,	e' '	•	•			•	•		•	•		UG-201A/U
Type N Cable		٩	٠	•	. •	٠	۰	٠	•		•	•	•	•	•	۰	•	•	•	•	HP 11500A



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ERRATA (Cont'd)

PERFORMANCE TESTS

PROCEDURE:

1. Connect the test setup as shown in Figure 4-8. Make the following control settings.

608E/F:

MODULATION

ATTENUATION

FREQUENCY RANGE

MEGACYCLES

State

FREQUENCY RANGE

MEGACYCLES

MEGACYCLES

Gamma

Multiplication

MEGACYCLES

MEGACYCLES

MEGACYCLES

MEGACYCLES

MEGACYCLES

MEGACYCLES

AMPLITUDE RANGE

MEGACYCLES

<

2. Adjust the generator signal level for 0 dBm (read on vector voltmeter).

3. Switch voltmeter to CHANNEL B. Signal at INPUT shall be at least 30 dB below signal at output.

Reverse Isolation: > 30 dB_____

Page 4-11, Table 4-1 Add:

4-10	Reverse Isolation		
3	Reverse Isolation	dB	30

Page 5-2, Paragraph 5-8:

Change "Figure 4-1" to "Figure 5-1" in step 1.

Page 6-2, Table 6-2:

Change C2 to 0160-2049, C:FXD 5000 PF +80 -20% 500 WVDC, 28480, 0160-2049.

Page 8-9, Figure 8-4:

Delete line connecting CR8 and C4.

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CHANGE 1

Page 6-4, Figure 6-1: Change to read as follows:

Figure	0 1	0	7	75 /
HIGHTP	h-1	. (:0)	ninet	Parts
- 10 mg 0/01 C	V. 20	· • • • • • •	0000000	A 000 00

1 2 3 4 5 6 7 8 8 8 9	$\begin{array}{c} 08747\text{-}0003\\ 5000\text{-}8766\\ 5060\text{-}0247\\ \hline \\ 5060\text{-}8553\\ 08447\text{-}00002\\ 5060\text{-}0727\\ 08447\text{-}00032\\ 08447\text{-}00033\\ 5000\text{-}8569\\ \end{array}$	DECK:MAIN SIDE COVER:3x8, OLIVE GRAY FRAME ASSY* NOT ASSIGNED TOP COVER:5x8, OLIVE GRAY REAR PANEL FOOT ASSY FRONT PANEL, MINT GRAY FRONT PANEL OPTION 001, MINT GRAY BOTTOM COVER, OLIVE GRAY
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CHANGE 2

Page 1-1, Table 1-1:

Change the following specifications:

Output Level: >+6 dBm at 1-dB compression point.

Distortion: Harmonics at least 32 dB down at output levels up to 0 dBm.

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Page 4-2, Paragraph 4-6:
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Change specification for "Output Level (Compression)" to >+6 dBm at 1-dB compression point.

Page 4-8, Paragraph 4-8:

Change specification to read:

Harmonics at least 32 dB down at output levels up to 0 dBm. Change 35 dB to 32 dB in Step 3 (two places).

Page 4-11, Paragraph 4-8: Change limit under "Min." to -32.

►CHANGE 3

Page 6-2, Table 6-2: Change A1CR9 to 1884-0073, THYRISTOR-SCR. Add A1R13, 0757-0401, R:FXD MET FLM 100 OHM 1% 1/8W.

Page 8-9, Figure 8-4:

Add resistor R13, 100 ohms, from junction of CR8 anode/CR9 gate to ground.

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Page 4-3, Paragraph 4-6, Step 5: Change "+7 dBm" to "+6 dBm" in first line.